Two-day National Workshop on Design Challenges in IoT and Machine Learning
23-01-2019 & 24-01-2019

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Sponsored by
COUNCIL OF SCIENTIFIC AND INDUSTRIAL RESEARCH

Organized By
Department of Electronics and Communication Engineering
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ONLINE REGISTRATION
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Sri Ramakrishna Institute of Technology (SRIT) is one of the technological institution of the prestigious SNR Sons Charitable Trust, Coimbatore. SRIT came into existence in the year 2002, approved by AICTE and affiliated to Anna University, Chennai. SRIT offers six undergraduate programme & four post graduate programme in Engineering & Technology and MBA. SRIT has an excellent infrastructure with state-of-the-art laboratories and library facilities with dedicated Faculty and able supporting personnel with well-connected transport system. SRIT has signed a MOU with Kyungpook National University-Korea, Infosys (Campus connect), NIIT, CISCO, BSNL, SREC-CSIR, Micromax Systems, SERTEL Electronics, Roots Industries, Salzer Electronics and CODISSIA, for collaborative research and academic development. Sri. R. Vijayakumhar, an eminent industrialist is the Managing Trustee of SNR Sons Charitable Trust, Sri. D. Lakshminarayanasamy is the Joint Managing Trustee and Mr. C. V. Ramkumar is the Chief Executive. Dr. M. Paulraj is at the helm of affairs as Principal of SRIT, managing the academic and research growth of the Institution.

ABOUT THE DEPARTMENT

The Electronics and Communication Engineering (ECE) Department has a rich tradition in research and teaching. The Department offers four year B.E. Electronics and Communication Engineering Programme with sanctioned intake of 60 from 2002-2003 onwards. The Department has increased its intake strength to 120 from 2005-2006 onwards. The Department offers M.E. Communication Systems with an intake of 18 from 2013-2014 onwards. All the class rooms are enabled with LCD projector facility. The department also has young, dynamic and energetic faculty members. They are devoted to serve the nation and community by generating and disseminating knowledge and technologies essential to the local and global needs in the field of Electronics and Communication Engineering. The Department also encourages students to take up Graduate Aptitude Test for Engineering (GATE) and Graduate Record Examination (GRE), during their final year to pursue their higher education in India or abroad.

ABOUT THE WORKSHOP

Rapid developments in hardware, software, and communication technologies have allowed the emergence of Internet-connected sensory devices that provide observation and data measurement from the physical world. Internet-connected devices technology, continues to extend the current Internet by providing connectivity and interaction between the physical and cyber worlds. In addition to increased volume, the IoT generates Big Data characterized by velocity in terms of time and location dependency, with a variety of multiple modalities and varying data quality. This workshop discusses the different machine learning methods that deal with the challenges in IoT data. The potential and challenges of machine learning for IoT data analytics will also be discussed. This would be a great platform for the interaction of students with reputed scientists and academicians. It also opens up vistas by disseminating the recent research for industrial applications. Further, it paves way for industrialists to innovate modern products which helps in ease of processing and help for the societal development.

ABOUT THE COLLEGE

The Electronics and Communication Engineering (ECE) Department has a rich tradition in research and teaching. The Department offers four year B.E. Electronics and Communication Engineering Programme with sanctioned intake of 60 from 2002-2003 onwards. The Department has increased its intake strength to 120 from 2005-2006 onwards. The Department offers M.E. Communication Systems with an intake of 18 from 2013-2014 onwards. All the class rooms are enabled with LCD projector facility. The department also has young, dynamic and energetic faculty members. They are devoted to serve the nation and community by generating and disseminating knowledge and technologies essential to the local and global needs in the field of Electronics and Communication Engineering. The Department also encourages students to take up Graduate Aptitude Test for Engineering (GATE) and Graduate Record Examination (GRE), during their final year to pursue their higher education in India or abroad.

KEY CONTENT

- An Introduction to IoT
- An Introduction to Machine Learning and its Algorithms
- Unsupervised Algorithms for IoT Analysis (From Modeling to visualization)
  - KMeans Clustering
  - KNN Algorithms
- Supervised Algorithms for IoT (From Modeling to Visualization)
  - Bayesian Classifier
  - Support Vector Machine
- Boosting Algorithms for IoT (From Modeling to Visualization)

RESOURCE PERSONS

- Mr. D. Rajkumar, Application Engineer
  DesignTech System Ltd., Chennai
- Dr. L. S. Jayashree, Professor/CSE
  PSG college of Technology, Coimbatore
- Dr. T. Senthil Kumar, Associate Professor/CSE
  Amrita Vishwa Vidyapeetham, Coimbatore

ELIGIBILITY

Faculty from Engineering colleges, Research scholars, UG/PG students.

IMPORTANT DATES

Last date for the Registration 18/1/2019
Intimation of Selection 21/1/2019

REGISTRATION FEE

- Internal Participants: ₹ 500/-
- External Participants: ₹ 750/-